

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10550323
	Filing Date		2005-09-23
	First Named Inventor	Roger D. Chamberlain	
	Art Unit	2431	
	Examiner Name	Henning, Matthew T.	
	Attorney Docket Number	53047-57370	

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	4	5740244		1998-04-14	Indeck, et al.		
	5	5978801		1999-11-02	Yuasa		
	6	5995963		1999-11-30	Nanba et al.		
	7	6175874		2001-01-16	Imai et al.		
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9	6389532		2002-05-14	Gupta et al.	
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20	7224185	B2	2007-05-29	Campbell et al.	
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8	20040162826		2004-08-19	Wyschogrod et al.	
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13	COMPTON ET AL., "Reconfigurable Computing: A Survey of Systems and Software", Technical Report, Northwestern University, Dept. of ECE, 1999, presented by Yi-Gang Tai	<input type="checkbox"/>
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16	EDGAR, "MUSCLE: Multiple Sequence Alignment with High Accuracy and High Throughput", Nucleic Acids Research, 2004, Vol. 32, No. 5, pp. 1792-1797	<input type="checkbox"/>
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28	JONES et al., "A Probabilistic Model of Information Retrieval: Development and Status", Information Processing and Management, August 1998, 76 pages	<input type="checkbox"/>
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35	ROY, "A bounded search algorithm for segmented channel routing for FPGA's and associated channel architecture issues", IEEE, Nov. 11, 1993, pp. 1695-1705, Vol. 12	<input type="checkbox"/>
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